



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Proell, *et al.* Docket No: INF-120
Filed: December 12, 2003 Art Unit: 2818
Serial No.: 10/733,332 Examiner: Lam, David
For: Integrated DRAM Semiconductor Memory and Method for Operating the Same

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

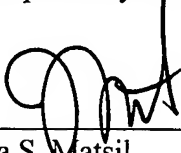
TRANSMITTAL OF REPLACEMENT SHEETS OF FORMAL DRAWINGS

Dear Sir:

Submitted herewith is one (1) replacement sheet of formal drawings for the above-identified application as requested by the Examiner in the Notice of Allowance dated January 13, 2005.

April 11, 2005
Date

Respectfully submitted,



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Certificate of Mailing via First Class Mail (37 C.F.R. § 1.8(a))

Date of Deposit: April 11, 2005

I hereby certify that the below listed correspondence is being deposited with the United States Postal Service on the date indicated above as first class mail in an envelope addressed to: Mail Stop Issue Fee, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450.

Certificate of Mailing via First Class Mail (1 page)
Part B - Fee Transmittal (1 original and 1 copy)
Transmittal of Replacement Sheets of Formal Drawings (1 page)
One (1) Sheet of Replacement Formal Drawings
Power of Attorney and Correspondence Address Indication Form (1 page)
Statement Under 37 CFR 3.73(b) (1 page)
Return Postcard

Respectfully submitted,

Kristin R. Hayes
Legal Assistant

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